

**IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Nicholas J. Kelsey, et al.
SERIAL NO.: 09/748,098
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TITLE: System and Method for Instruction Level Multithreading in an
Embedded Processor Using Zero-Time Context Switching
EXAMINER: David J. Huisman
GROUP ART UNIT: 2183
ATTY. DKT. NO.: 20880-05093

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Dated: September 17, 2008

By: /Brian G. Brannon/

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AMENDMENT I

SIR:

Responsive to the Office Action dated March 17, 2008, please enter the following amendments, and consider the accompanying remarks. Applicant respectfully requests reconsideration of the claims in the above-identified application in view of the following amendments and remarks for the purpose of putting the application in condition for allowance.

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Claims begin on page 4 of this paper.

Remarks begin on page 16 of this paper.